## **Measuring Method of Stray Inductance for Inverter Circuit**

In evaluating the characteristics of IGBTs, stray inductance of the test circuit is a major factor to be considered. This document presents the measurement method of the stray inductance (inclusive of the module's own internal inductance). In evaluation of IGBT, one phase circuit of inverter, as shown in Fig. 1, is generally utilized. The circuit is basically a half–bridge topology, composed of two series connected IGBT modules, a power supply (PS) and an inductive load (L), where the total stray inductance of the main circuit is symbolized as Ls.

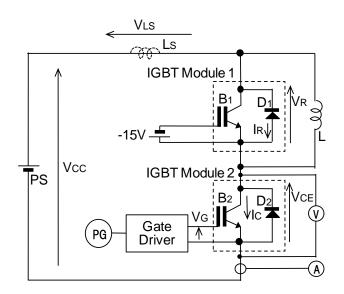


Fig.1. One phase circuit of inverter

The operational timing pattern of the circuit for measuring Ls is given in Fig.2 (a). The voltage and current waveforms of the pattern are shown in Fig.2 (b) and (c) respectively. In accordance with the pattern, IGBT B<sub>2</sub>, i.e. Module 2, is operated. From t =0 to t<sub>1</sub> the state of B<sub>2</sub> is ON and current Ic flows through load L and IGBT B<sub>2</sub>, as shown in Fig.3. In this case, the load current  $I_{\perp}$ which flows through inductive load L has the same value as the collector current Ic of B<sub>2</sub> where Ic increases with time.

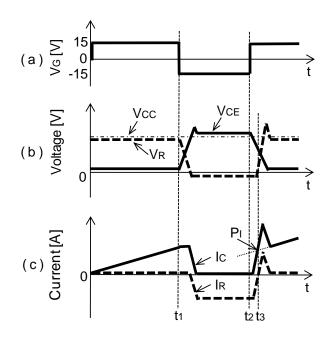


Fig.2. Operation pattern for measuring Ls

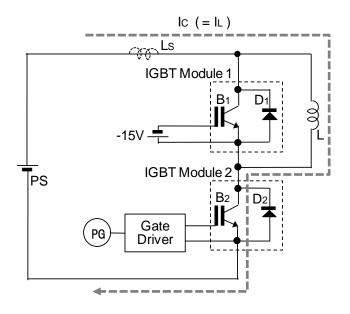


Fig.3. Current flow in the ON-state of B2

After t<sub>1</sub>, B<sub>2</sub> shifts to OFF-state through a transient period. During the OFF-state of B<sub>2</sub>, the current Ic is blocked but the load current IL is maintained as a circulating current through diode D<sub>1</sub> as shown in Fig.4. After t<sub>2</sub>, B<sub>2</sub> turns ON during a transient period.

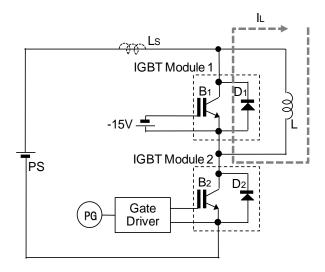


Fig.4. Current flow in the OFF-state of B2

Just after t<sub>1</sub> and t<sub>2</sub>, there appears transient state, where both Ic and I<sub>L</sub> currents flow, as shown in Fig.5. The reverse current (I<sub>R</sub>) of D<sub>1</sub> is the difference between Ic and I<sub>L</sub>, that is to say I<sub>R</sub>=(I<sub>C</sub> - I<sub>L</sub>).

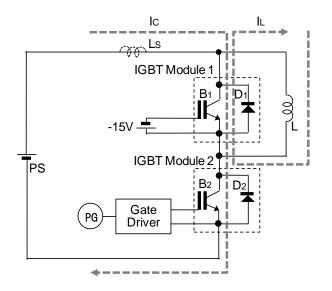


Fig.5. Current flow during transient period

Focusing on the transient period following t<sub>2</sub>, Ic flows through D<sub>1</sub> and B<sub>2</sub> as shown in Fig.6. During this state, Ls is calculated by the following equation (1).

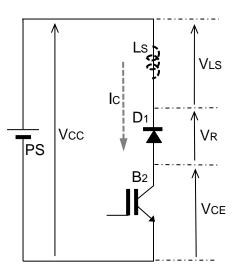


Fig.6. Simplified circuit for transient period

If VCE and dIc/dt are detected at a time point where VR becomes zero, that is, IR = 0, the following equation (2) can be derived from equation (1).

Therefore, Ls can be specified using the measured voltage  $V_{CE}$  and current Ic as arranged in Fig.1. The voltage meter must be connected to sense terminals of IGBT module in order to accurately measure the value of Ls including the internal inductance of the module. The time point symbolized as by t<sub>3</sub> in Fig.2 (c) can be detected through the intersection point P<sub>1</sub> by checking the waveform of Ic.