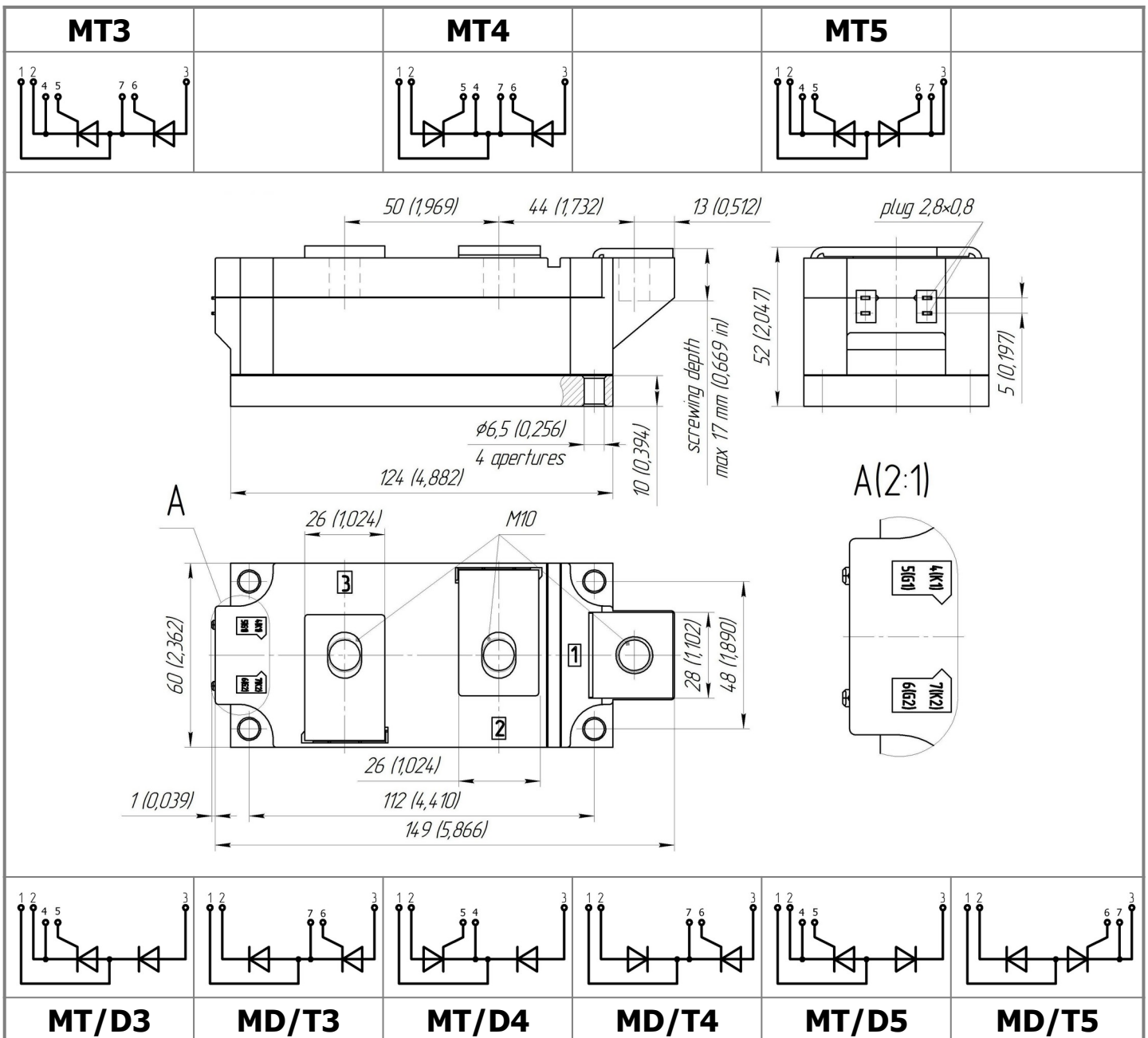


Electrically isolated base plate  
 Industrial standard package  
 Simplified mechanical design, rapid assembly  
 Pressure contact

# Double Thyristor Module For Phase Control MTx-260-44-A2

Mean on-state current			$I_{TAV}$	260 A
Repetitive peak off-state voltage			$V_{DRM}$	3800 ÷ 4400 V
Repetitive peak reverse voltage			$V_{RRM}$	
Turn-off time			$t_q$	500 $\mu$ s
$V_{DRM}, V_{RRM}, V$	3800	4000	4200	4400
Voltage code	38	40	42	44
$T_j, ^\circ C$	- 40 ÷ 125			



All dimensions in millimeters (inches)

## MAXIMUM ALLOWABLE RATINGS

Symbols and parameters		Units	Values	Test conditions
<b>ON-STATE</b>				
$I_{TAV}$	Mean on-state current	A	260	$T_c=85\text{ }^\circ\text{C}$ ; 180° half-sine wave; 50 Hz
$I_{TRMS}$	RMS on-state current	A	405	
$I_{TSM}$	Surge on-state current	kA	5.0 6.0	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=10\text{ ms}$ ; single pulse; $V_D=V_R=0\text{ V}$ ; Gate pulse: $I_G=2\text{ A}$ ; $t_{GP}=50\text{ }\mu\text{s}$ ; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
			5.5 6.5	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=8.3\text{ ms}$ ; single pulse; $V_D=V_R=0\text{ V}$ ; Gate pulse: $I_G=2\text{ A}$ ; $t_{GP}=50\text{ }\mu\text{s}$ ; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
$I^2t$	Safety factor	$A^2s\cdot 10^3$	120 180	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=10\text{ ms}$ ; single pulse; $V_D=V_R=0\text{ V}$ ; Gate pulse: $I_G=2\text{ A}$ ; $t_{GP}=50\text{ }\mu\text{s}$ ; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
			120 170	$T_j=T_{j\text{ max}}$ $T_j=25\text{ }^\circ\text{C}$ 180° half-sine wave; $t_p=8.3\text{ ms}$ ; single pulse; $V_D=V_R=0\text{ V}$ ; Gate pulse: $I_G=2\text{ A}$ ; $t_{GP}=50\text{ }\mu\text{s}$ ; $di_G/dt\geq 1\text{ A}/\mu\text{s}$
<b>BLOCKING</b>				
$V_{DRM}, V_{RRM}$	Repetitive peak off-state and Repetitive peak reverse voltages	V	3800÷4400	$T_{j\text{ min}} < T_j < T_{j\text{ max}}$ ; 180° half-sine wave; 50 Hz; Gate open
$V_{DSM}, V_{RSM}$	Non-repetitive peak off-state and Non-repetitive peak reverse voltages	V	3900÷4500	$T_{j\text{ min}} < T_j < T_{j\text{ max}}$ ; 180° half-sine wave; single pulse; Gate open
$V_D, V_R$	Direct off-state and Direct reverse voltages	V	$0.6\cdot V_{DRM}$ $0.6\cdot V_{RRM}$	$T_j=T_{j\text{ max}}$ ; Gate open
<b>TRIGGERING</b>				
$I_{FGM}$	Peak forward gate current	A	8	$T_j=T_{j\text{ max}}$
$V_{RGM}$	Peak reverse gate voltage	V	5	
$P_G$	Gate power dissipation	W	4	$T_j=T_{j\text{ max}}$ for DC gate current
<b>SWITCHING</b>				
$(di_T/dt)_{crit}$	Critical rate of rise of on-state current non-repetitive (f=1 Hz)	A/ $\mu\text{s}$	400	$T_j=T_{j\text{ max}}$ ; $V_D=0.67\cdot V_{DRM}$ ; $I_{TM}=2 I_{TAV}$ ; Gate pulse: $I_G=2\text{ A}$ ; $t_{GP}=50\text{ }\mu\text{s}$ ; $di_G/dt\geq 2\text{ A}/\mu\text{s}$
<b>THERMAL</b>				
$T_{stg}$	Storage temperature	$^\circ\text{C}$	-40 ÷ 50	
$T_j$	Operating junction temperature	$^\circ\text{C}$	-40 ÷ 125	
$T_{c\text{ op}}$	Operating temperature	$^\circ\text{C}$	-40 ÷ 125	
<b>MECHANICAL</b>				
a	Acceleration under vibration	$\text{m}/\text{s}^2$	50	

## CHARACTERISTICS

Symbols and parameters		Units	Values	Conditions	
<b>ON-STATE</b>					
$V_{TM}$	Peak on-state voltage, max	V	2.30	$T_j=25\text{ }^\circ\text{C}; I_{TM}=628\text{ A}$	
$V_{T(TO)}$	On-state threshold voltage, max	V	1.40	$T_j=T_{j\text{ max}};$	
$r_T$	On-state slope resistance, max	m $\Omega$	1.300	$0.5\pi I_{TAV} < I_T < 1.5\pi I_{TAV}$	
$I_L$	Latching current, max	mA	1000	$T_j=25\text{ }^\circ\text{C}; V_D=12\text{ V};$ Gate pulse: $I_G=2\text{ A};$ $t_{GP}=50\text{ }\mu\text{s}; di_G/dt\geq 1\text{ A}/\mu\text{s}$	
$I_H$	Holding current, max	mA	300	$T_j=25\text{ }^\circ\text{C};$ $V_D=12\text{ V};$ Gate open	
<b>BLOCKING</b>					
$I_{DRM}, I_{RRM}$	Repetitive peak off-state and Repetitive peak reverse currents, max	mA	200	$T_j=T_{j\text{ max}};$ $V_D=V_{DRM}; V_R=V_{RRM}$	
$(dv_D/dt)_{crit}$	Critical rate of rise of off-state voltage <sup>1)</sup> , min	V/ $\mu\text{s}$	1000	$T_j=T_{j\text{ max}};$ $V_D=0.67\cdot V_{DRM};$ Gate open	
<b>TRIGGERING</b>					
$V_{GT}$	Gate trigger direct voltage, max	V	4.00 2.50 2.00	$T_j=T_{j\text{ min}}$ $T_j=25\text{ }^\circ\text{C}$ $T_j=T_{j\text{ max}}$	$V_D=12\text{ V}; I_D=3\text{ A};$ Direct gate current
$I_{GT}$	Gate trigger direct current, max	mA	400 250 200	$T_j=T_{j\text{ min}}$ $T_j=25\text{ }^\circ\text{C}$ $T_j=T_{j\text{ max}}$	
$V_{GD}$	Gate non-trigger direct voltage, min	V	0.35	$T_j=T_{j\text{ max}};$ $V_D=0.67\cdot V_{DRM};$	
$I_{GD}$	Gate non-trigger direct current, min	mA	15.00	Direct gate current	
<b>SWITCHING</b>					
$t_{gd}$	Delay time	$\mu\text{s}$	3.50	$T_j=25\text{ }^\circ\text{C}; V_D=1500\text{ V}; I_{TM}=I_{TAV};$ $di/dt=200\text{ A}/\mu\text{s};$ Gate pulse: $I_G=2\text{ A}; V_G=20\text{ V};$ $t_{GP}=50\text{ }\mu\text{s}; di_G/dt=2\text{ A}/\mu\text{s}$	
$t_q$	Turn-off time <sup>2)</sup> , max	$\mu\text{s}$	500	$dv_D/dt=50\text{ V}/\mu\text{s}; T_j=T_{j\text{ max}}; I_{TM}=I_{TAV};$ $di_R/dt=10\text{ A}/\mu\text{s}; V_R=100\text{ V};$ $V_D=0.67\text{ }V_{DRM};$	
$Q_{rr}$	Total recovered charge, max	$\mu\text{C}$	1850	$T_j=T_{j\text{ max}}; I_{TM}=400\text{ A};$	
$t_{rr}$	Reverse recovery time, max	$\mu\text{s}$	41	$di_R/dt=-5\text{ A}/\mu\text{s};$	
$I_{rrM}$	Peak reverse recovery current, max	A	90	$V_R=100\text{ V}$	
<b>THERMAL</b>					
$R_{thjc}$	Thermal resistance, junction to case				
	per module	$^\circ\text{C}/\text{W}$	0.0340	180° half-sine wave, 50 Hz	
	per arm	$^\circ\text{C}/\text{W}$	0.0680		
	per module	$^\circ\text{C}/\text{W}$	0.0325	DC	
per arm	$^\circ\text{C}/\text{W}$	0.0650			
$R_{thch}$	Thermal resistance, case to heatsink				
	per module	$^\circ\text{C}/\text{W}$	0.0100		
	per arm	$^\circ\text{C}/\text{W}$	0.0200		
<b>INSULATION</b>					
$V_{ISOL}$	Insulation test voltage	kV	3.00	Sine wave, 50 Hz; RMS	t=60 sec
			3.60		t=1 sec
<b>MECHANICAL</b>					
$M_1$	Mounting torque (M6) <sup>3)</sup>	Nm	6.00	Tolerance $\pm 15\%$	
$M_2$	Terminal connection torque (M10) <sup>3)</sup>	Nm	12.00	Tolerance $\pm 15\%$	
w	Weight, max	g	1500		

**PART NUMBERING GUIDE**

MT	3	-	260	-	44	-	A2	E2	-	A2	-	N
1	2		3		4		5	6		7		8

1. Thyristor module (MT)  
Thyristor – Diode module (MT/D)  
Diode – Thyristor module (MD/T)
2. Circuit Schematic:  
3 – serial connection  
4 – common Cathode  
5 – common Anode
3. Average On-state Current, A
4. Voltage Code
5. Critical rate of rise of off-state voltage
6. Group of turn-off time ( $dv_D/dt=50 \text{ V}/\mu\text{s}$ )
7. Package Type (M.A2)
8. Ambient Conditions:  
N – Normal

**NOTES**

<sup>1)</sup> Critical rate of rise of off-state voltage

Symbol of group	A2
$(dv_D/dt)_{crit}, \text{ V}/\mu\text{s}$	1000

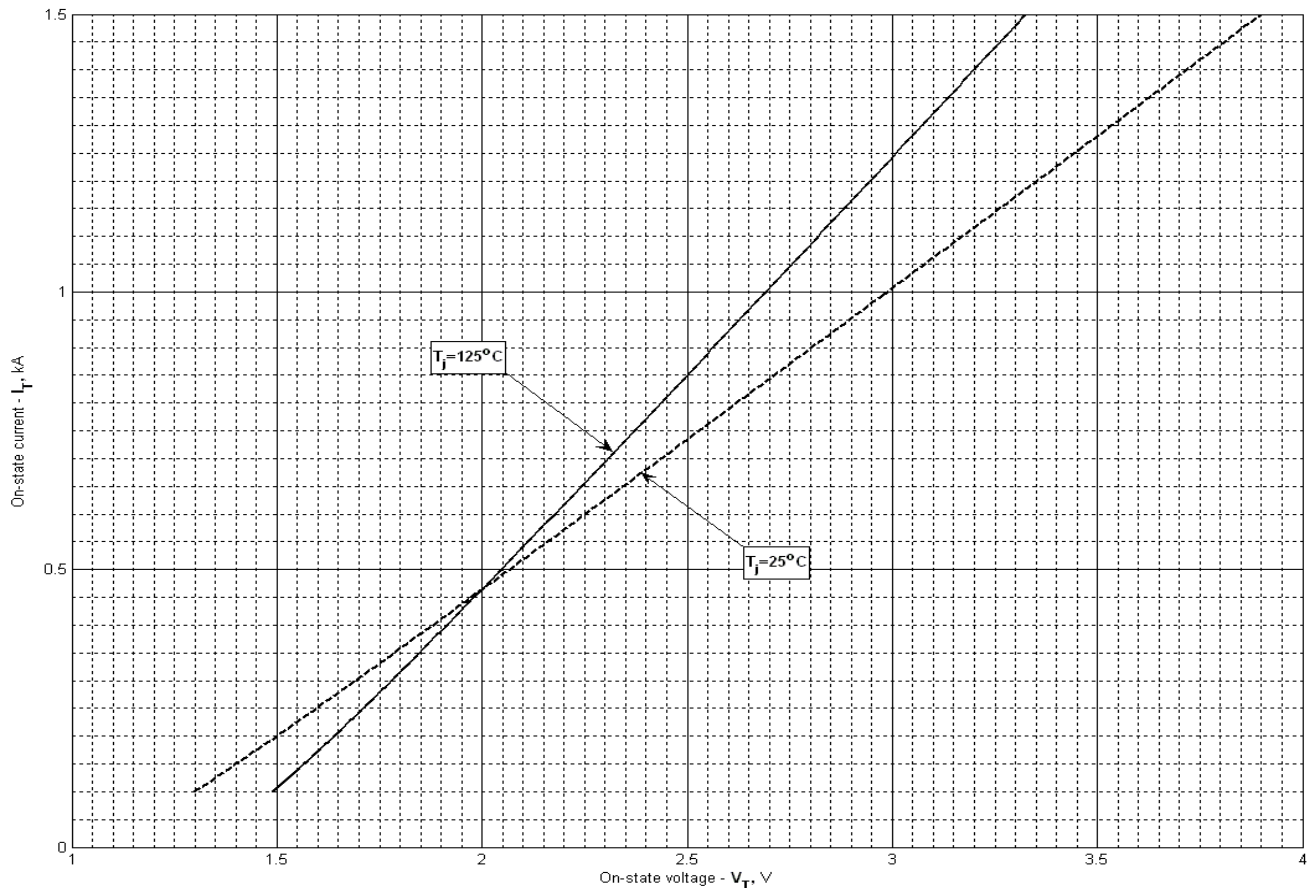
<sup>2)</sup> Turn-off time ( $dv_D/dt=50 \text{ V}/\mu\text{s}$ )

Symbol of group	E2
$t_{q}, \mu\text{s}$	500

<sup>3)</sup> The screws must be lubricated



UL certified file-No. E255404



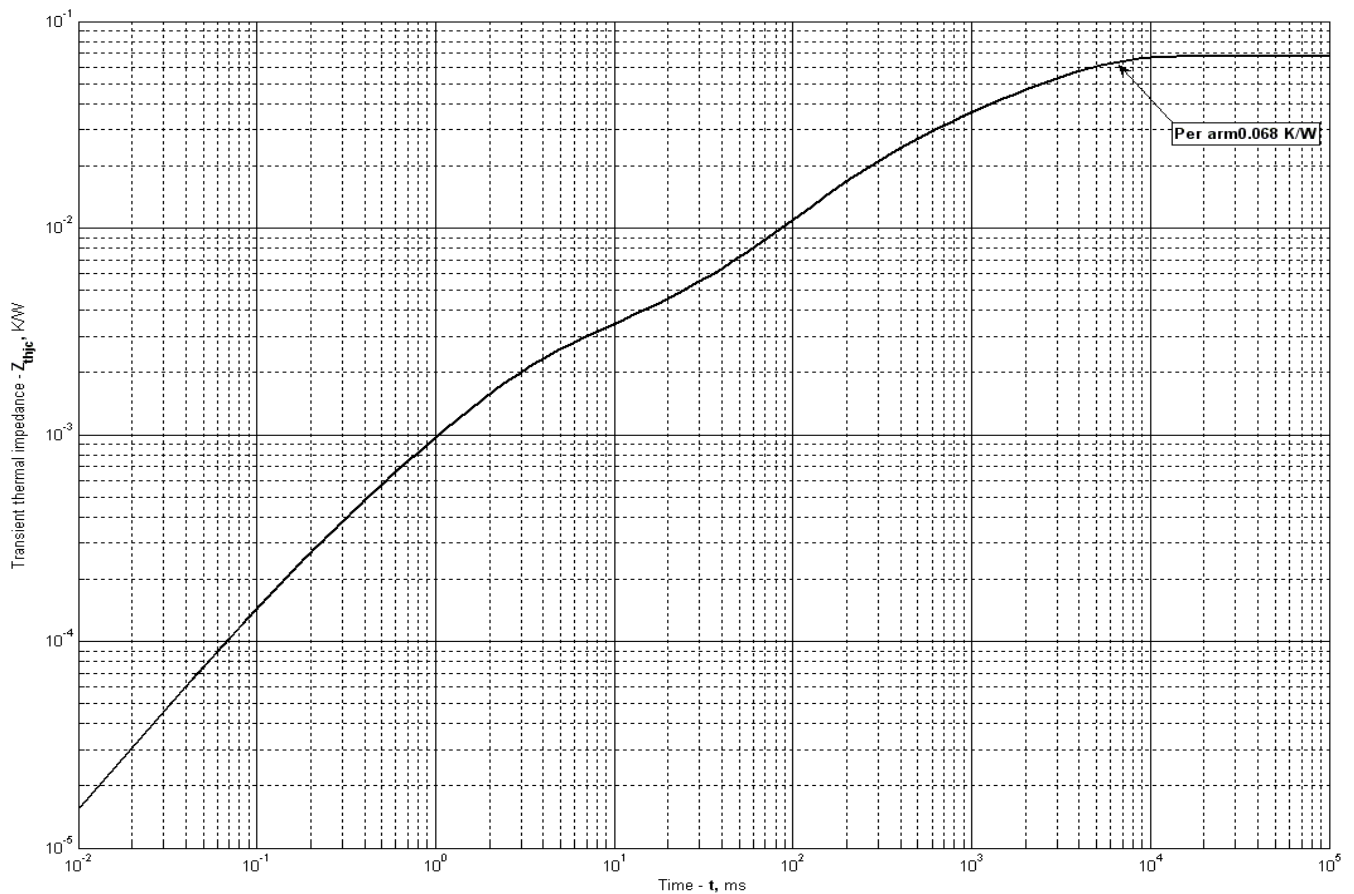
**Fig 1 – On-state characteristics of Limit device**

Analytical function for On-state characteristic:

$$V_T = A + B \cdot i_T + C \cdot \ln(i_T + 1) + D \cdot \sqrt{i_T}$$

	Coefficients for max curves	
	$T_j = 25^\circ\text{C}$	$T_j = T_{j\text{max}}$
<b>A</b>	1.046892	1.269338
<b>B</b>	1.774285	1.200656
<b>C</b>	-0.178926	-0.238969
<b>D</b>	0.289862	0.387132

**On-state characteristic model (see Fig. 1)**



**Fig 2 – Transient thermal impedance**

Analytical function for Transient thermal impedance junction to case  $Z_{thjc}$  for DC:

$$Z_{thjc} = \sum_{i=1}^n R_i \left( 1 - e^{-\frac{t}{\tau_i}} \right)$$

Where  $i = 1$  to  $n$ ,  $n$  is the number of terms in the series.

$t$  = Duration of heating pulse in seconds.

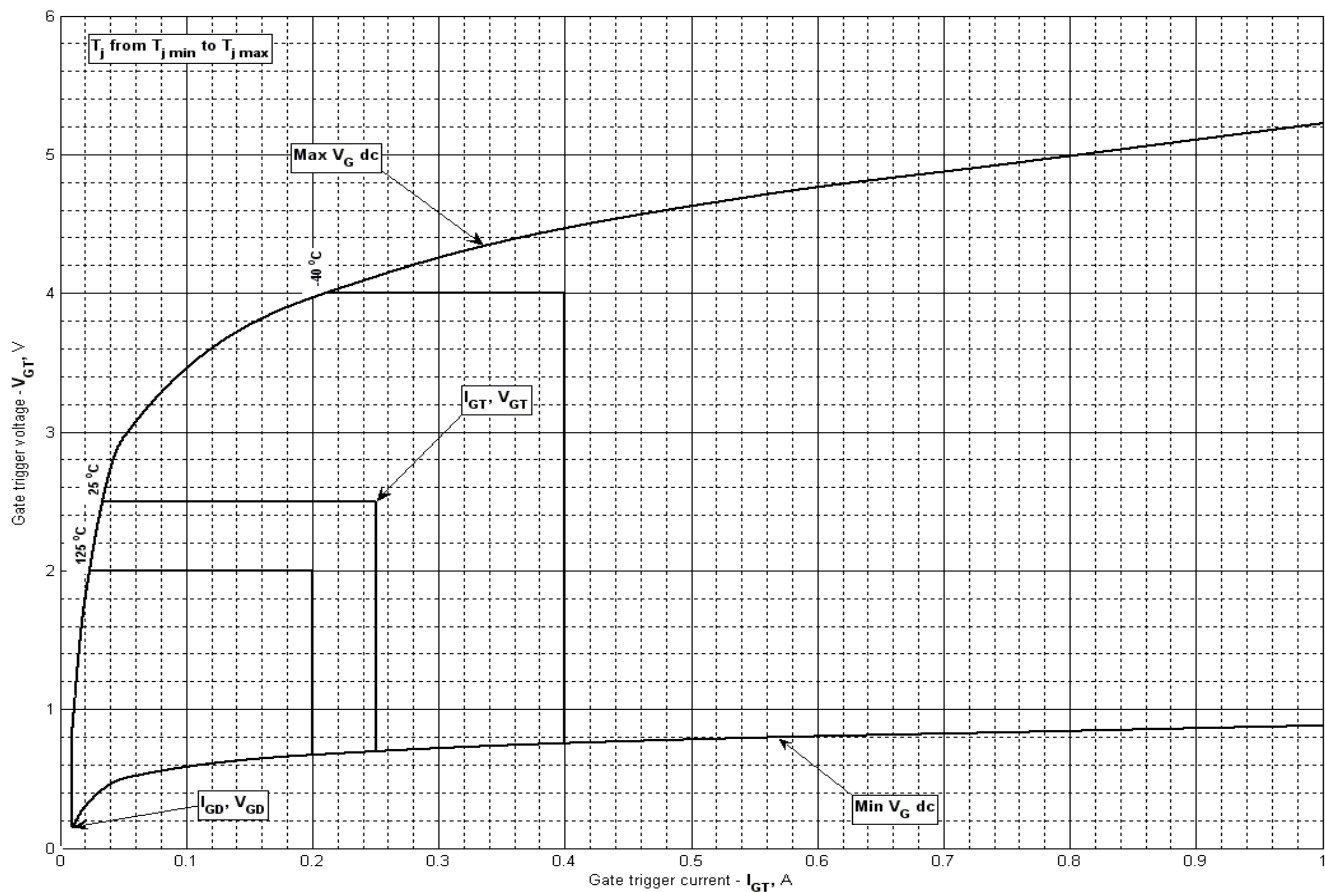
$Z_{thjc}$  = Thermal resistance at time  $t$ .

$R_i$  = Amplitude of  $p_{th}$  term.

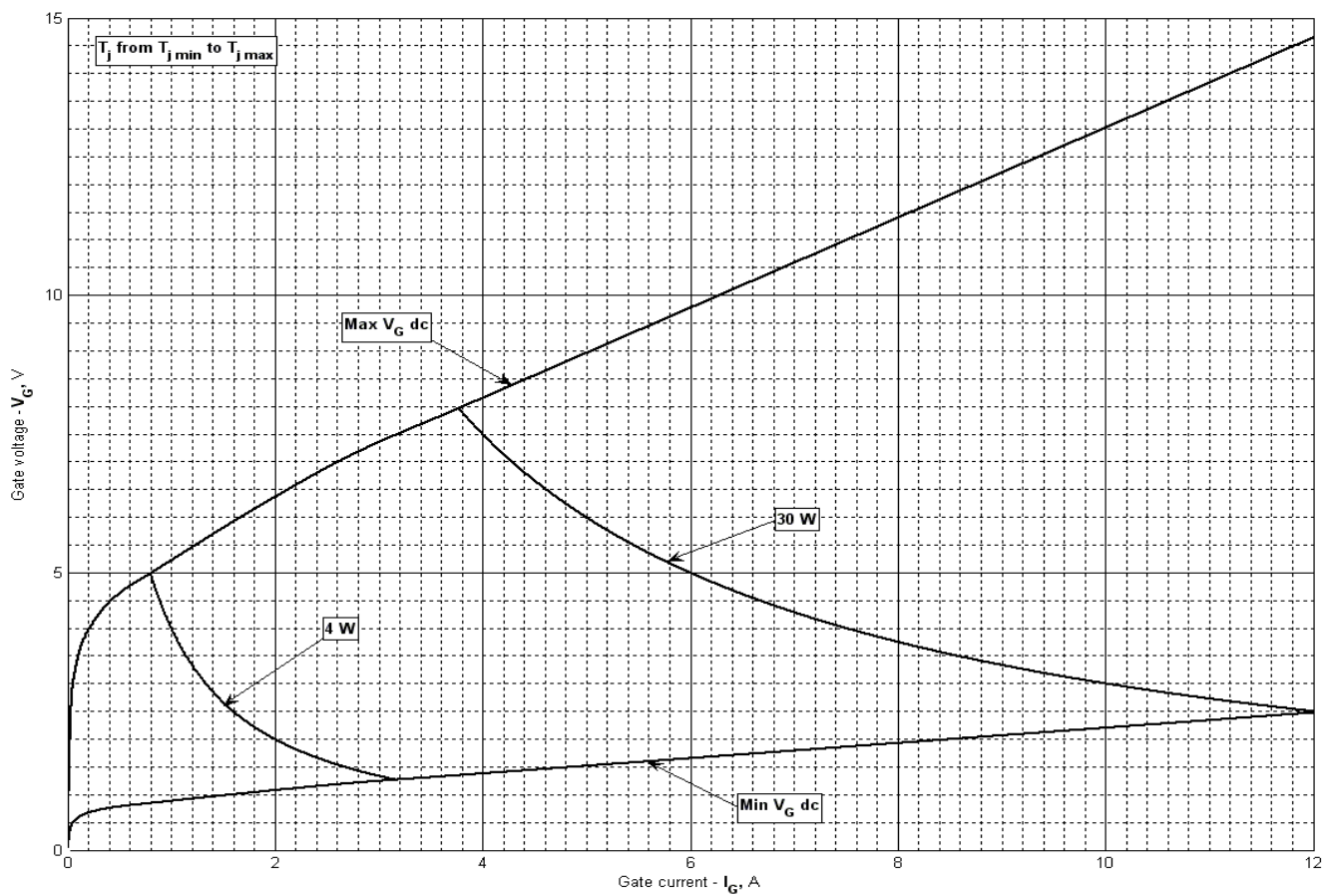
$\tau_i$  = Time constant of  $r_{th}$  term.

<b>i</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>
<b><math>R_i</math>, K/W</b>	0.0385	0.01253	0.0144	0.0007273	0.001871	0.0001367
<b><math>\tau_i</math>, s</b>	3.124	0.8558	0.1999	0.009185	0.002295	0.000238

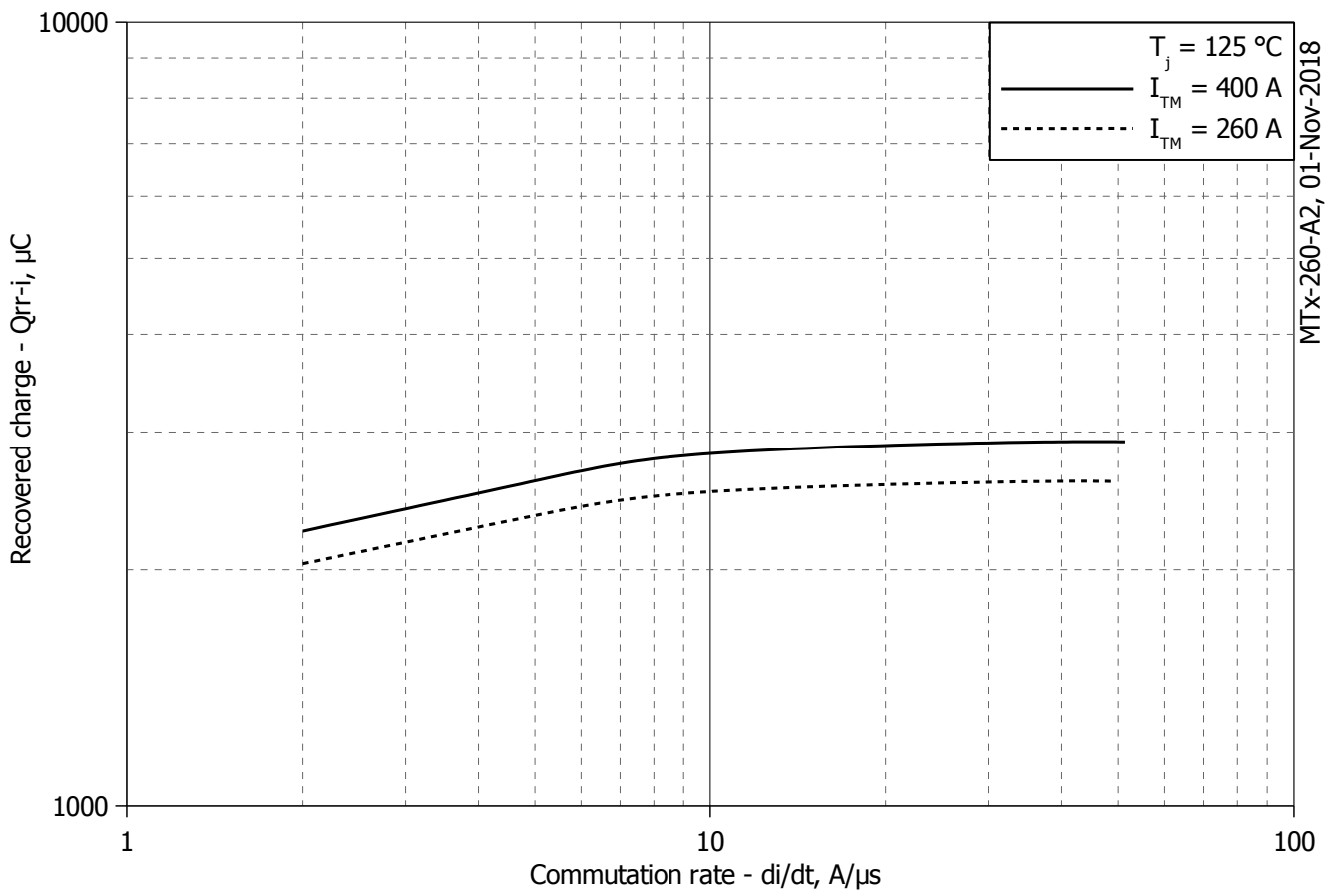
**Transient thermal impedance junction to case  $Z_{thjc}$  model (see Fig. 2)**



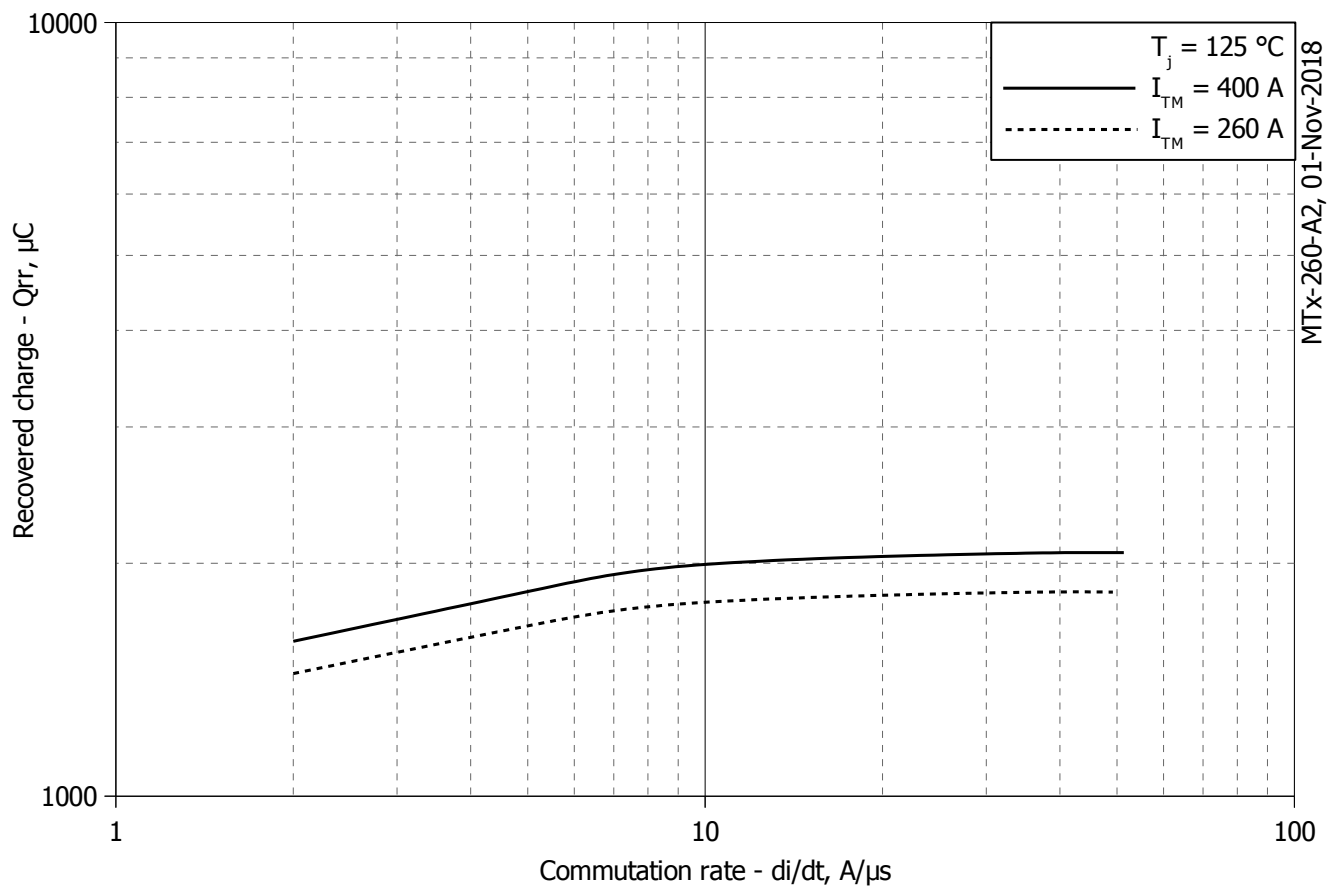
**Fig 3 – Gate characteristics – Trigger limits**



**Fig 4 - Gate characteristics – Power curves**

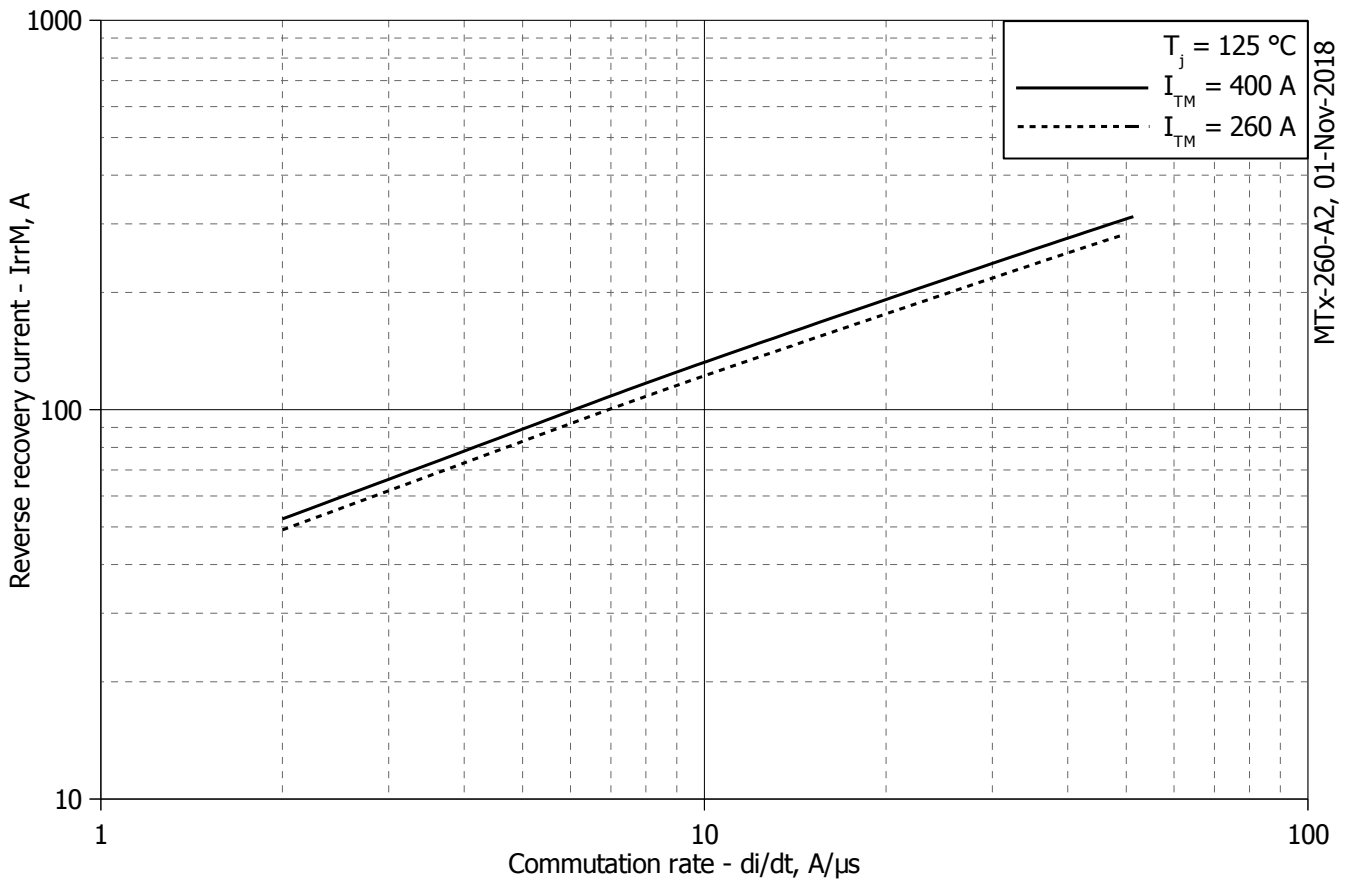


**Fig 5 - Total recovered charge,  $Q_{rr-i}$  (integral)**

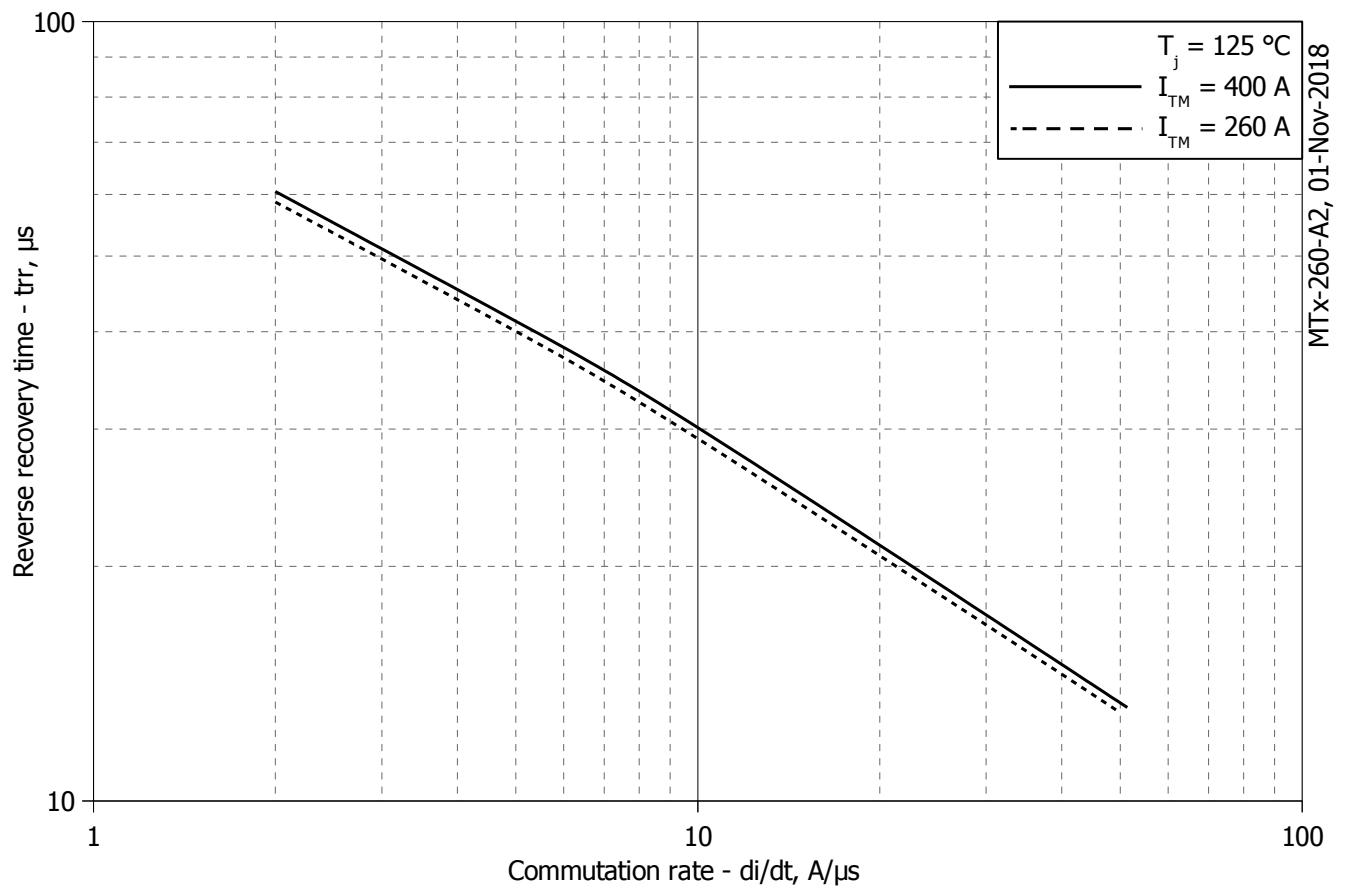


**Fig 6 - Recovered charge,  $Q_{rr}$  (25% chord)**

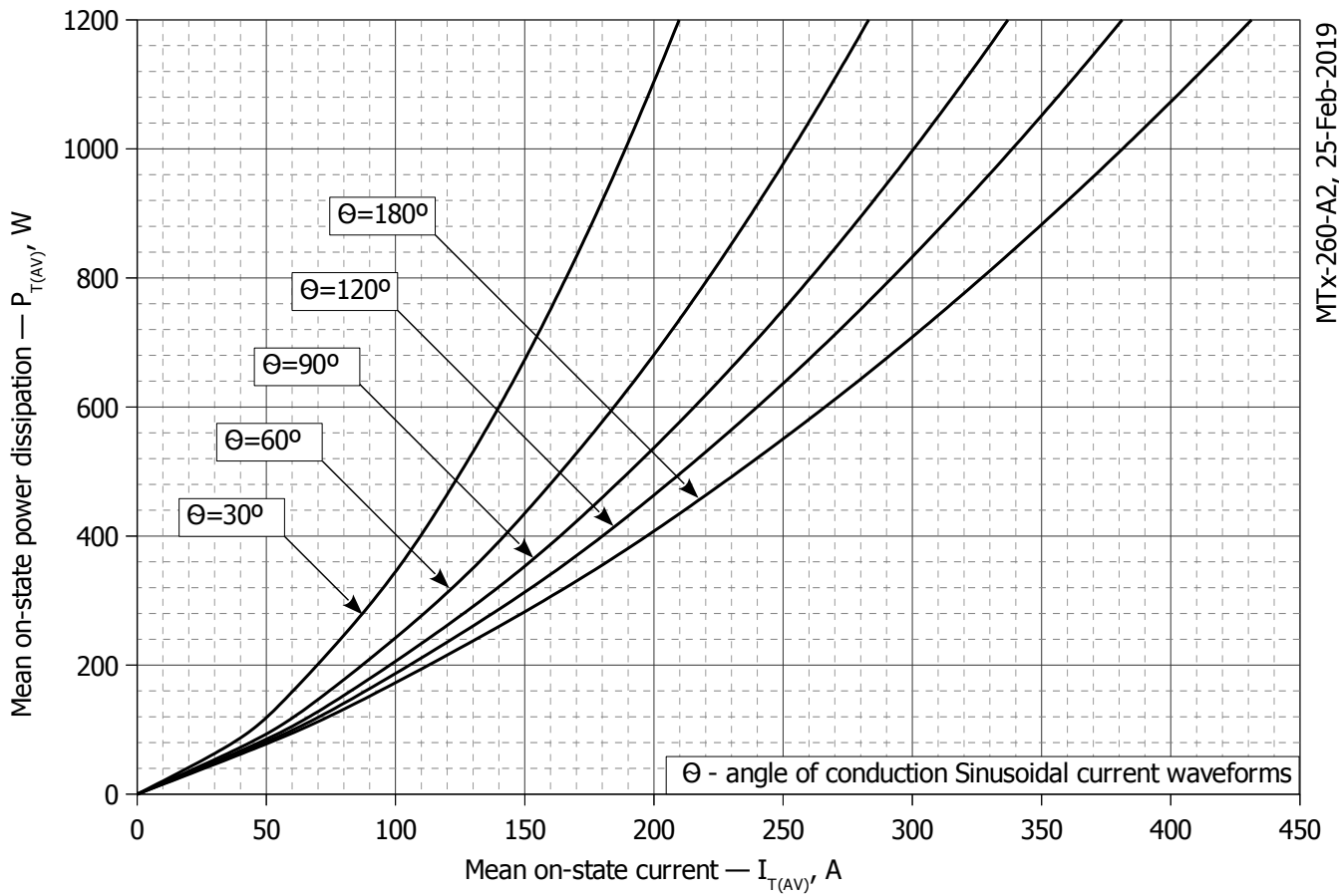




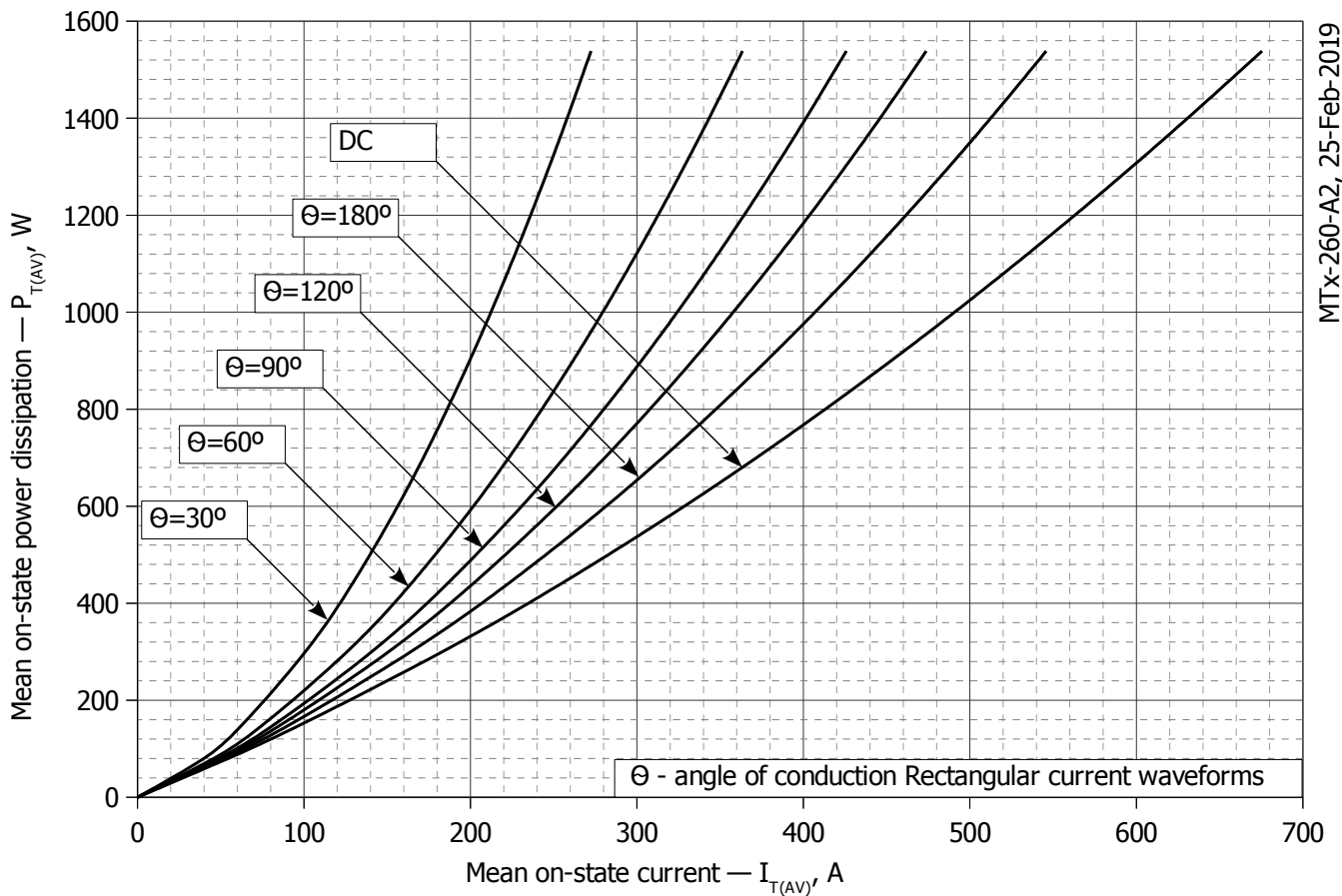
**Fig 7 - Peak reverse recovery current,  $I_{rrM}$**



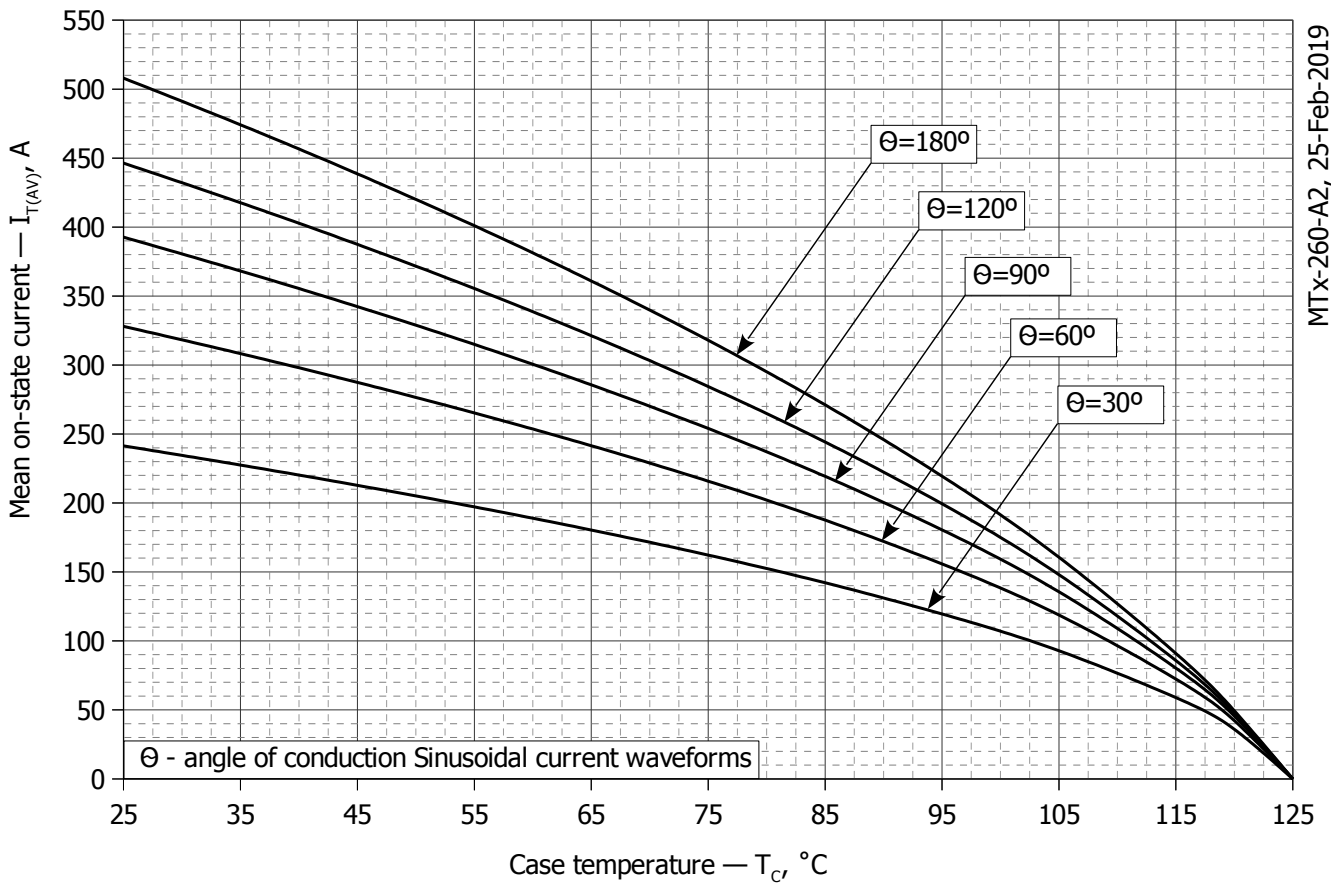
**Fig 8 - Maximum recovery time,  $t_{rr}$  (25% chord)**



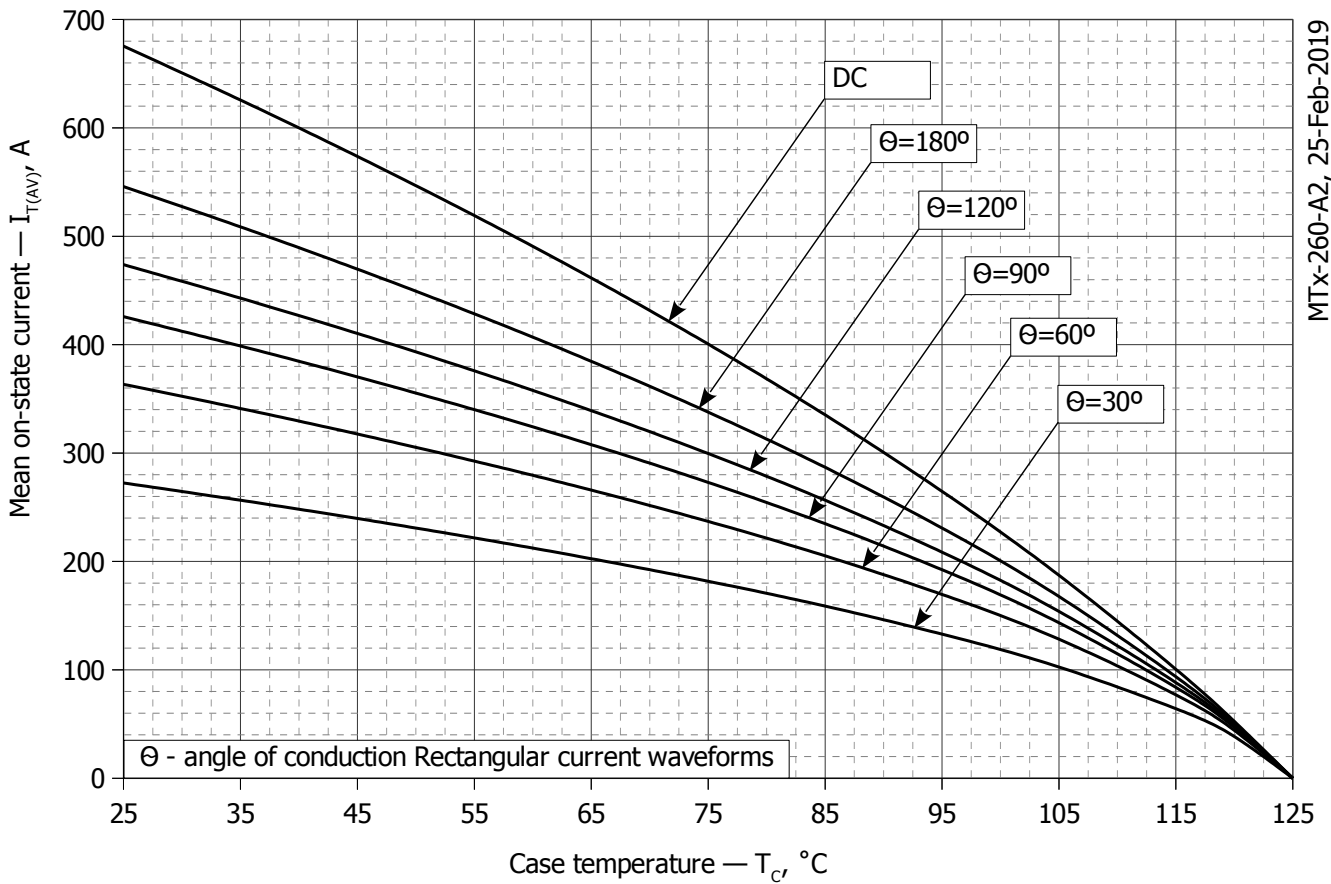
**Fig 9 – On-state power loss (sinusoidal current waveforms)**



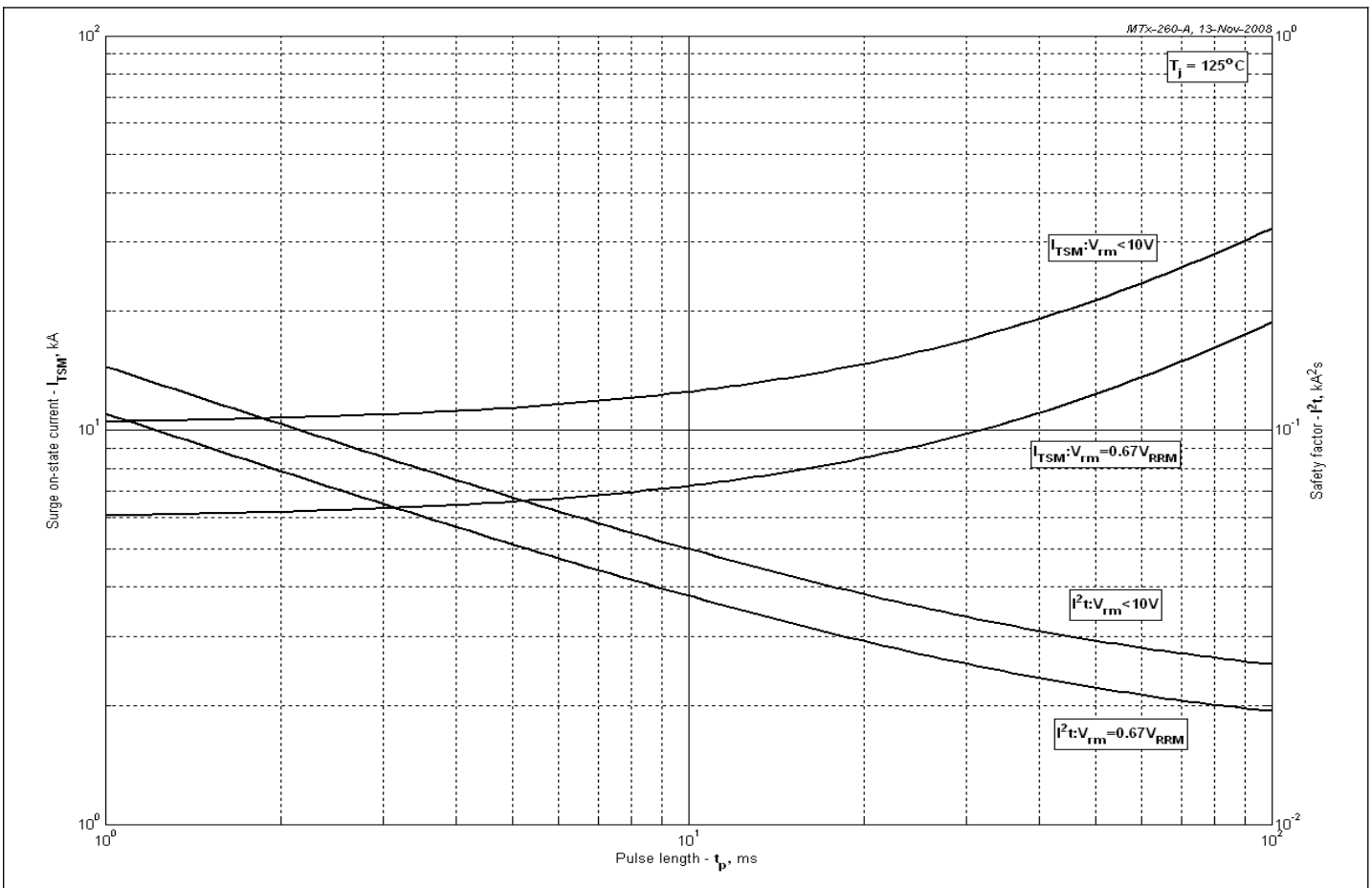
**Fig 10 - On-state power loss (rectangular current waveforms)**



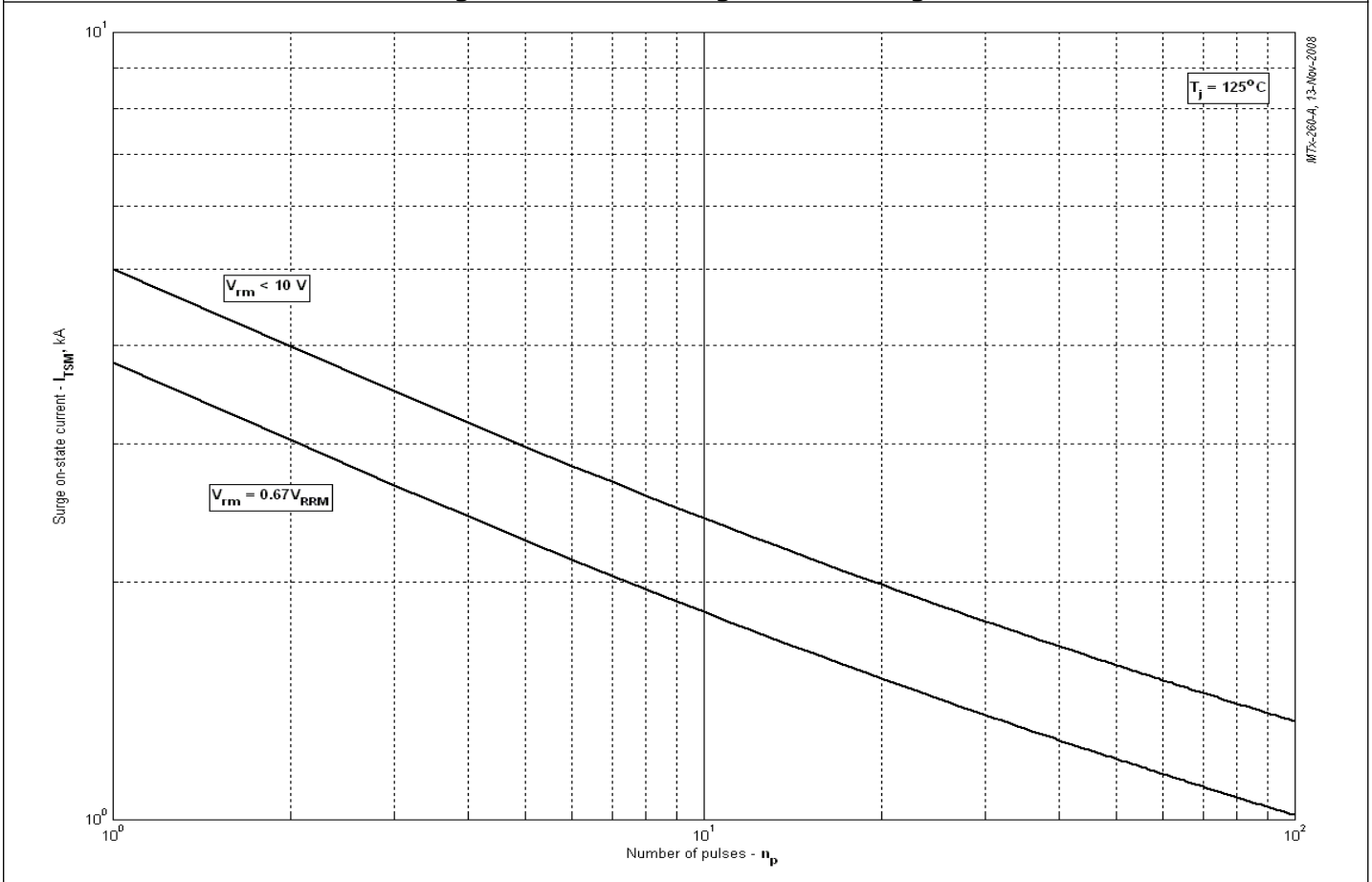
**Fig 11 – Maximum case temperature (sinusoidal current waveforms)**



**Fig 12 - Maximum case temperature (rectangular current waveforms)**



**Fig 13 – Maximum surge and  $I^2t$  ratings**



**Fig 14 - Maximum surge ratings**